



PATENT APPLICATION

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RESPONSE UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
TECHNOLOGY CENTER ART UNIT 2831

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Yukie NAKANO et al.

Application No.: 09/865,575

Filed: May 29, 2001

Group Art Unit: 2831

Examiner: E. Thomas

Docket No.: 109639

For: MULTILAYER CERAMIC CAPACITOR AND PRODUCTION METHOD THEREOF

AMENDMENT AFTER FINAL REJECTION UNDER 37 C.F.R. § 1.116

Director of the U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

In reply to the September 4, 2002, Office Action and the interview held
September 27, 2002, please amend the above-identified application as follows:

IN THE CLAIMS:

Please cancel claims 2 and 7 without prejudice to or disclaimer of the subject matter
contained therein.

Please replace claim 1 as follows:

1. (Twice Amended) A multilayer ceramic capacitor comprising:
- internal electrode layers; and
 - dielectric layers, the dielectric layers comprising particles, wherein an average particle diameter (R), in a direction parallel with said internal electrode layers, is larger than a thickness (d) of said dielectric layer, wherein a ratio (R/d) between said average particle diameter (R) and the thickness (d) of said dielectric layer satisfies $1 < R/d < 3$, and wherein the thickness (d) of said dielectric layer is less than 3 μm .